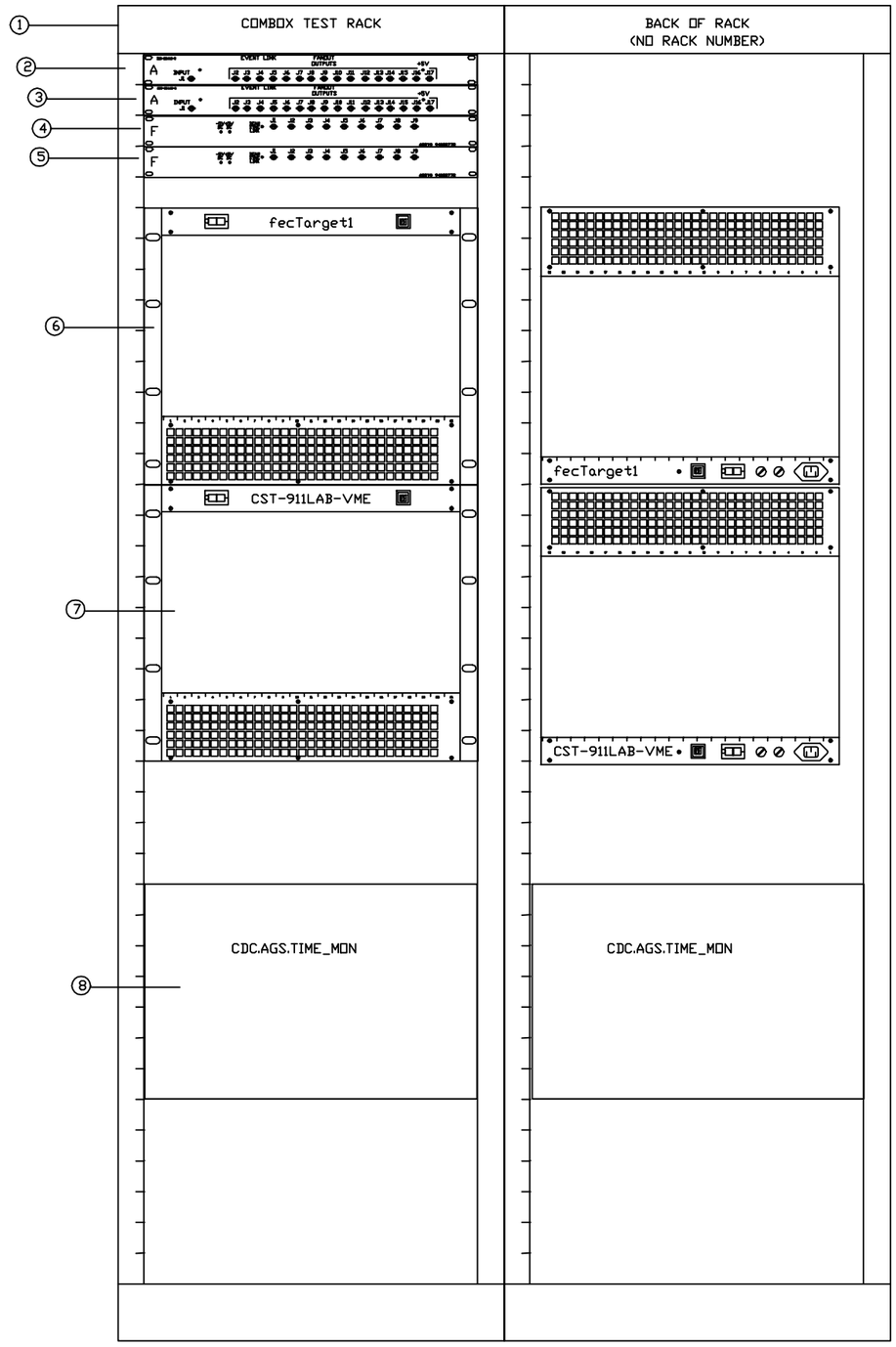
REV.	ZONE	ECN NO.	DATE	BY	CKR	APP.

BNL, BNL 12/28/00 8:07 AM D09-E-RD2772.dwg



QTY REQD -3	QTY REQD -2	QTY REQD -1	REF DESIGNATOR	ITEM NO.	DESCRIPTION	MAT'L DWG NO./BNL NO.
			14	13		
			13	12		
			12	11		
			11	10		
			9	9		
			8	8		
			7	7		
		1	8	6	CDC.AGS.TIME_MON MB CHASSIS	
		1	7	5	6U VME CHASSIS CST-911LAB-VME	
		1	6	4	fecTarget1 VME chassis	
		2	4,5	3	BEAM SYNC PANEL	
		2	2,3	2	EVENTLINK FANOUTS	
		1	1	1	70" RACK	

INTERPRET IN GENERAL ACCORDANCE WITH ASME Y14.24M-1989

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

DECIMAL TOLERANCES
.X ±.030
.XX ±.015
.XXX ±.005
ANGULAR TOLERANCE
± .5°

OUTSTANDING ECN

NA FINISH

BREAK SHARP EDGES
MAX. XX MIN. XX

Q.A. CATEGORY A-3

		DATE
DRAWN BY	Jappe	1/25/00
CHECKED BY		
DESIGN APPROVAL		
ENGINEER APPROVAL		
SUPVR. APPROVAL		

NEXT ASSEMBLY

BROOKHAVEN NATIONAL LABORATORY
UPTON, N.Y. 11973

CA Division

911C LABRack Layout

CST-911LAB-VME

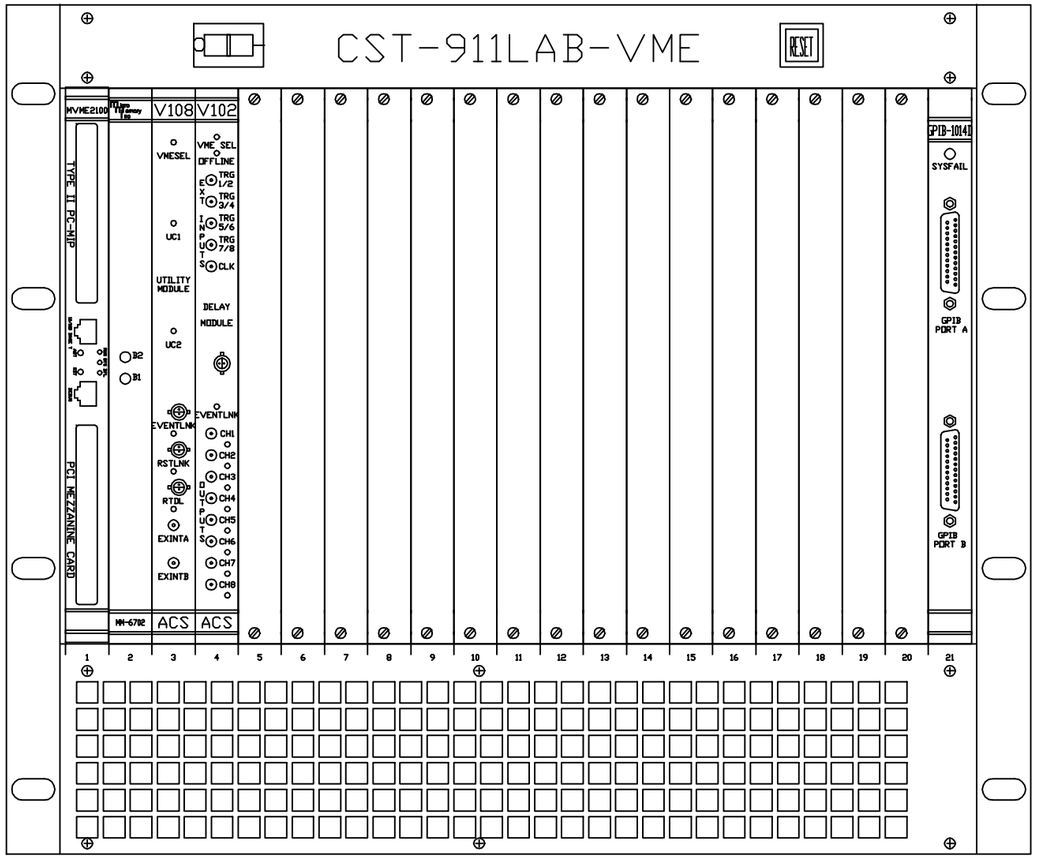
SIZE A | DRAWING NUMBER D09-E-RD2772 | REV. A

MATERIAL: ——— | SCALE: FULL | WEIGHT: – | SHEET 1

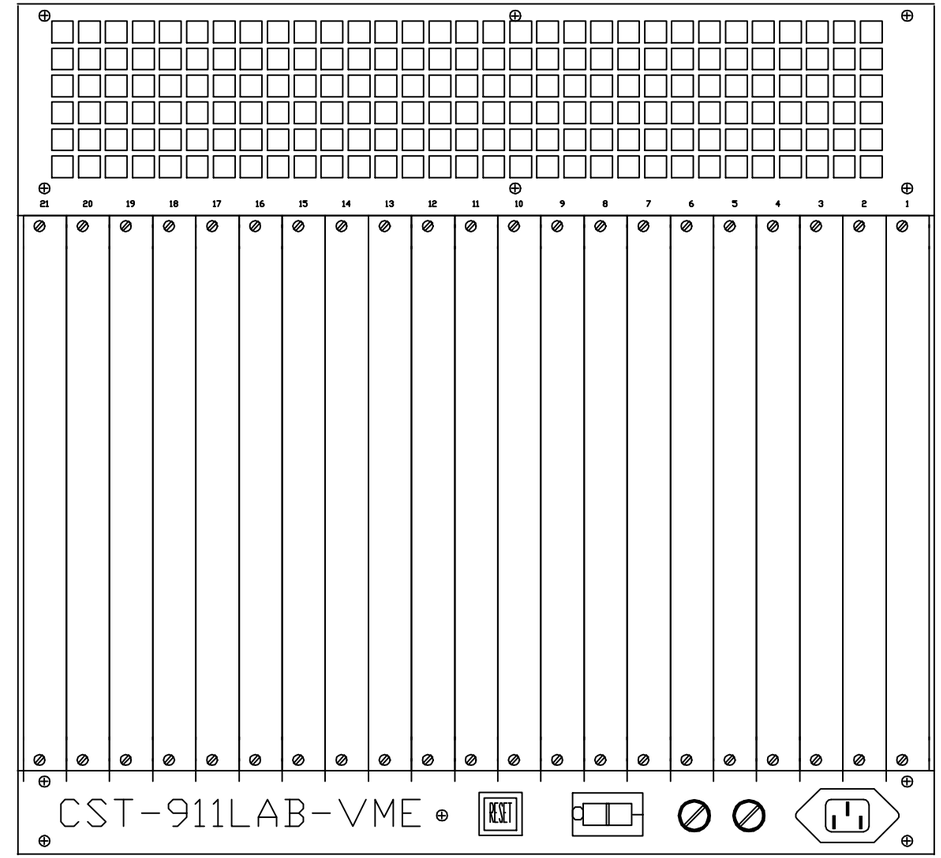
1 *AUTOCAD

REV.	ZONE	ECN NO.	DATE	BY	CKR	APP.

BNL, BNL 12/28/00 8:10 AM D09-E-RD2773.dwg



FRONT VIEW



REAR VIEW

NOTE:

1. ALL UNUSED SLOTS, FRONT AND REAR

-3	-2	-1	REF DESIGNATOR	ITEM NO.	DESCRIPTION	MAT'L DWG NO./BNL NO.
		-	-	-	-	-
		1	SLOT 21	6	ALLEN BRADLEY 1014D 488 GPIB CONTRLLER	-
		1	SLOT 4	5	V102 DELAY MODULES	94028072
		1	SLOT 3	4	V108 UTILITY MODULE	94028094
		1	SLOT 2	3	MEMORY BOARD	MM-6702
		1	SLOT 1	2	FRONT END COMPUTER	POWER3E
		1	N/A	1	VME CHASSIS	RHIC-93008000

QTY REQD

INTERPRET IN GENERAL ACCORDANCE WITH ASME Y14.24M-1989

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

DECIMAL TOLERANCES
.X ±.030
.XX ±.015
.XXX ±.005
ANGULAR TOLERANCE
± .5°

OUTSTANDING ECN

NA FINISH

BREAK SHARP EDGES
MAX. XX MIN. XX

Q.A. CATEGORY A-3		DATE
DRAWN BY	Jappe	1/18/2000
CHECKED BY		
DESIGN APPROVAL		
ENGINEER APPROVAL		
SUPVR. APPROVAL		

NEXT ASSEMBLY

BROOKHAVEN NATIONAL LABORATORY
UPTON, N.Y. 11973

CAD CONTROLS

BLDG. 911C COMBOX TEST RACK

VME CHASSIS CST-911LAB-VME

SIZE A | DRAWING NUMBER D09-E-RD2773 | REV. A

MATERIAL: ——— SCALE: NA WEIGHT: - SHEET 2

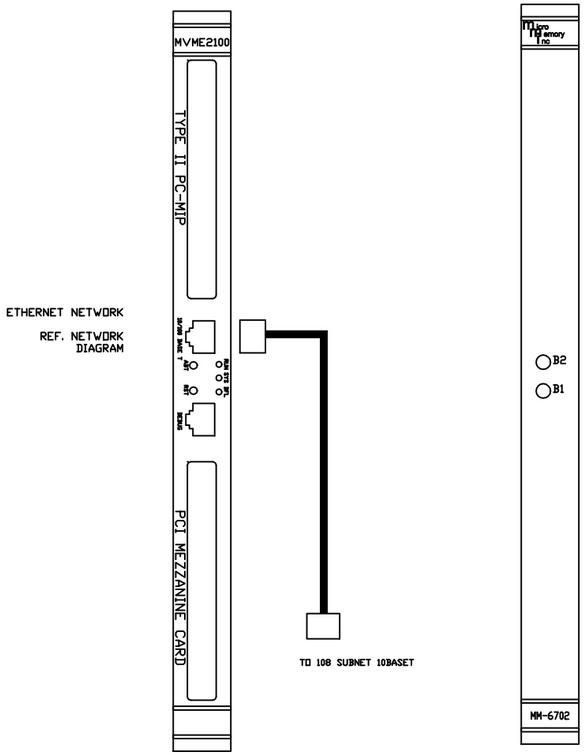
*AUTOCAD

REV.	ZONE	ECN NO.	DATE	BY	CKR	APP.

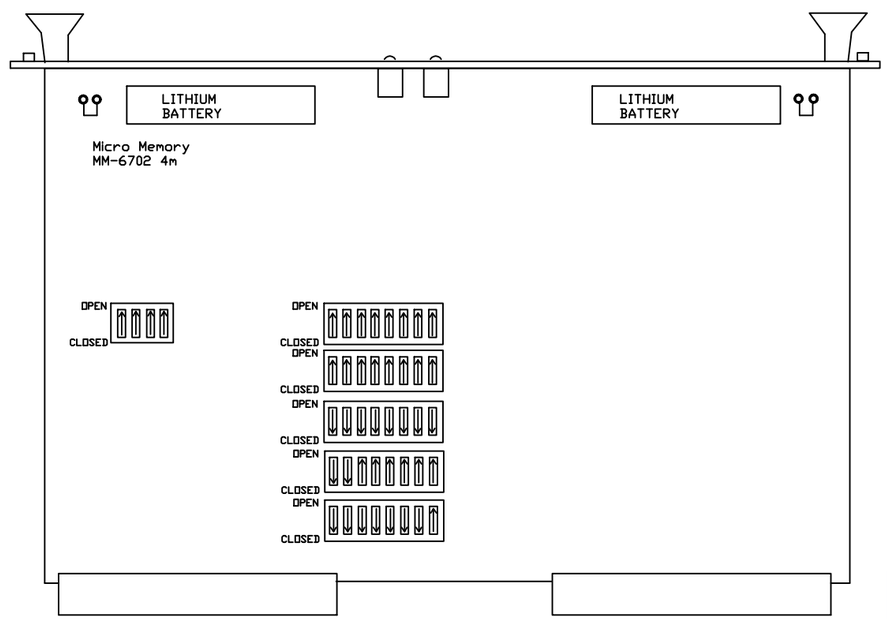
BNL, BNL 12/28/00 8:12 AM D09-E-RD2774.dwg

FRONT END COMPUTER
BLDG #: 911C
RACK #: XXXX
CHASSIS ID: CST-911LAB-VME
SLOT#: 1
FEC NAME: CST-911LAB-VME

DEVICE: MM-6702 MEMORY MODULE
BLDG #: 911C
RACK #: XXXX
CHASSIS ID: CST-911LAB-VME
SLOT #: 2
A24 BASE ADDRESS: XXX



JUMPER LIST
1. REMOVE E1.
2. INSTALL E15.
3. SW3 CLOSE ALL POSITIONS.
4. SW2 CLOSE POSITIONS 7 AND 8.
5. SW1 CLOSE POSITIONS 2-8.
6. INSTALL E14 AND E25 TO ENABLE BATTERIES



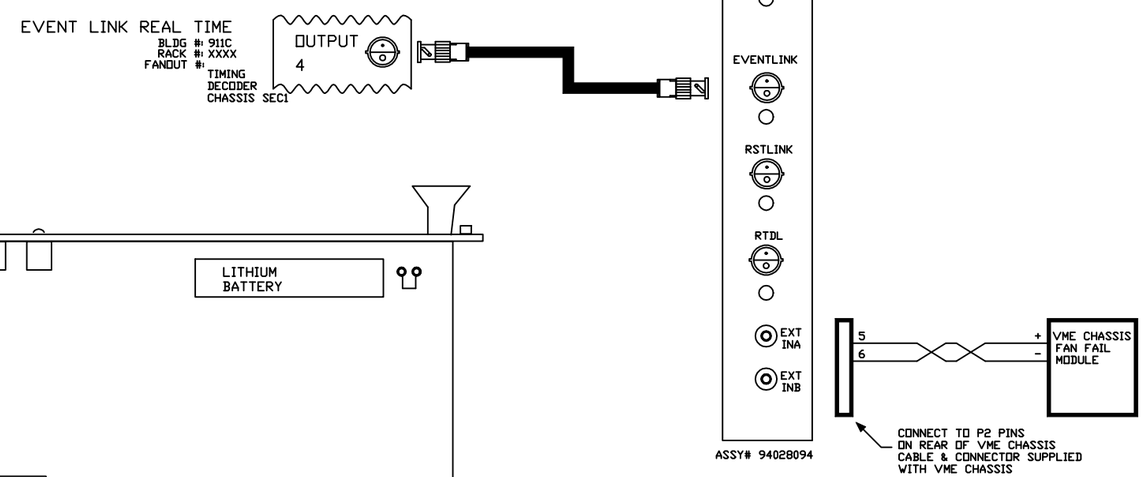
DEVICE: V108 UTILITY MODULE
BLDG #: 911C
RACK #: XXXX
CHASSIS ID: CST-911LAB-VME
SLOT #: 3
A24 BASE ADDRESS: 0XF0004000

JUMPER LIST

E25 through E50 are configured for remote reset address at the time of board acceptance test.
DO NOT MODIFY!

Remote reset pulls VME SYS Reset.
E58-E60, E59-E61

A24 BASE ADDRESS= 4000h
E57-E13, E2-E14, E3-E15, E4-E16, E5-E17, E6-E18
E7-E19, E8-E20, E9-E21, E10-E22, E11-E24, E57-E23
SW1=OPEN
SW2=CLOSED
SW3=OPEN
SW4=OPEN



TO SET UP BOOT PARAMS FOR THE FEC
ON A X-TERM RUN StartUp
Top of StartUp

go to diagnostics
then Servers
then AGS VME stations
then pick a station you want the params for
then fec diagnostics
then Show Boot Params (PPC) (power PC)

-3	-2	-1	REF DESIGNATOR	ITEM NO.	DESCRIPTION	MAT'L. DWG NO./BNL NO.
QTY REQD						

INTERPRET IN GENERAL ACCORDANCE WITH ASME Y14.24M-1989
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
DECIMAL TOLERANCES .X ±XXX .XX ±XXX .XXX ±XXX
ANGULAR TOLERANCE ± XXX
NA FINISH
BREAK SHARP EDGES MAX. XX MIN. XX
OUTSTANDING ECN

Q.A. CATEGORY A-3
DRAWN BY Jappe DATE 1/26/2000
CHECKED BY
DESIGN APPROVAL
ENGINEER APPROVAL
SUPVR. APPROVAL
NEXT ASSEMBLY

BROOKHAVEN NATIONAL LABORATORY
ASSOCIATED UNIVERSITIES, INC.
UPTON, N.Y. 11973

CAD WIRING DIAGRAM
911C VME CHASSIS CST-911LAB-VME
CPU/MEMORY /V108 UTIL MODULE

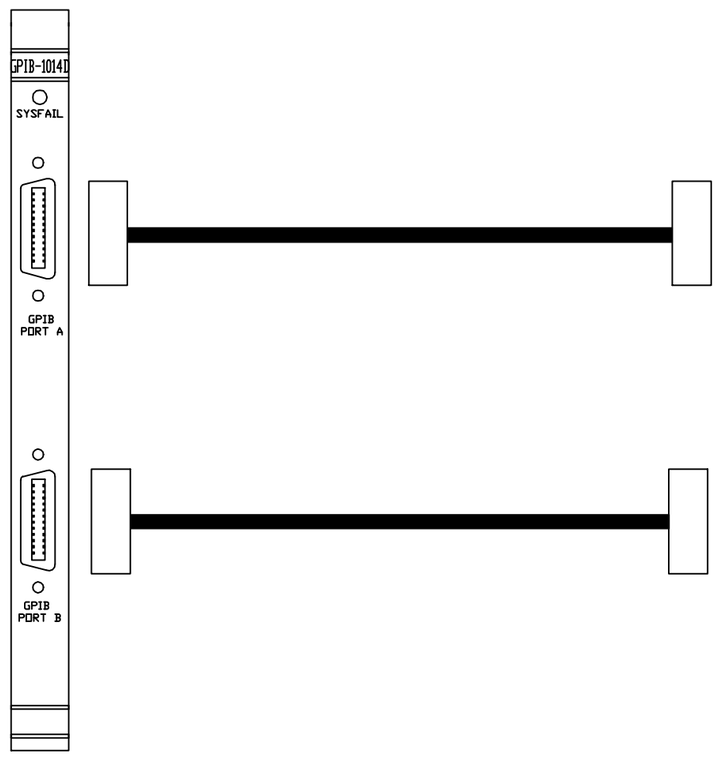
SIZE A | D09-E-RD2774 | REV. A
DRAWING NUMBER
MATERIAL: ——— SCALE: NA WEIGHT: – SHEET 3

*AUTOCAD

REV.	ZONE	ECN NO.	DATE	BY	CKR	APP.

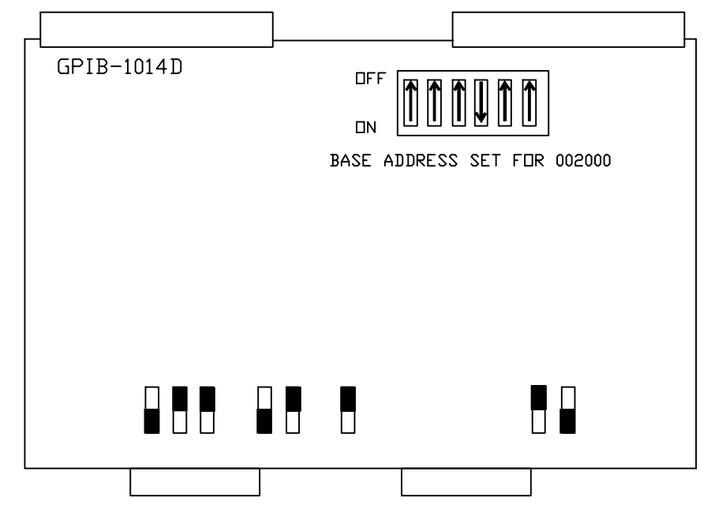
BNL, BNL 12/28/00 8:13 AM D09-E-RD2775.dwg

GPIB
BLDG #: 911C
RACK #: xxxx
CHASSIS ID: CST-911LAB-VME
SLOT #: 21
BASE ADDRESS:



TO SUPERCYLE DEVICE
CONTROLLER BLDG 911C
LAB 1

TO CDC.RT GENERATOR BLDG
911C LAB1



-3	-2	-1	REF DESIGNATOR	ITEM NO.	DESCRIPTION	MAT'L. DWG NO./BNL NO.
QTY REQD						

INTERPRET IN GENERAL ACCORDANCE WITH ASME Y14.24M-1989

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
DECIMAL TOLERANCES .X ±.030 .XX ± .XXX ±
ANGULAR TOLERANCE ±

NA FINISH
BREAK SHARP EDGES MAX. XX MIN. XX
OUTSTANDING ECN

Q.A. CATEGORY A-3		DATE
DRAWN BY	Warren Jappe	1/26/00
CHECKED BY		
DESIGN APPROVAL		
ENGINEER APPROVAL		
SUPVR. APPROVAL		

NEXT ASSEMBLY

BROOKHAVEN NATIONAL LABORATORY
UPTON, N.Y. 11973

CAD WIRING DIAGRAM
BLDG. 911C CST-911LAB-VME
GPIB wiring detail

SIZE A | DRAWING NUMBER D09-E-RD2775 | REV. A

MATERIAL: n/a SCALE: n/a WEIGHT: - SHEET 4

*AUTOCAD